

特開平1-157180(4)

この4冊もまた、第3回は第2回巻の
続プロットである。

代理人 齊藤 金 山 幸 郎

同様に低コストで、フ
 ードの1プレームを
 用ひ、1ラスタを所
 有するメモリを用い、フ
 ードを1ラスタ分
 だけ、次のインター
 フェイスから1ラスタ
 分のデータを1ラスタ
 分のメモリに読み込
 んで、メモリに読み
 込んだデータを1ラ
 スタ分のメモリに
 書き込む。このとき、

第 6 スキャンコンバータの
断、第 2 図は写真機例に
はれよび機を電気のロント

- 264 -

PUB-NO	APPL-DESCRIPTOR	APPL-NO	APPL-DATE
JP 01157180A	N/A	1987 JP-0315712	December 14, 1987

Details

Text

Image

HTML

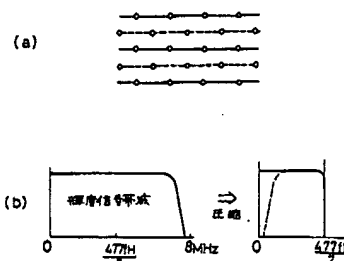
FULL

	U	1	Document ID	Issue Date	
2	<input type="checkbox"/>	<input type="checkbox"/>	US 4677482 A	19870630	Dual mod switching
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	JP 01157180 A	19890620	Interlacing interlacing image is
4			JP 03268737 A	19911129	LIVING B

特開平1-23678

[illegible]

第 1 圖



第 2 题

第 5 圖

	U	1	Document ID	Issue Date	
13	<input type="checkbox"/>	<input type="checkbox"/>	EP 369285 A	19900523	Picture processing system applying signal - avoids pixel errors arising and converted progressive video
14	<input type="checkbox"/>	<input type="checkbox"/>	JP 2002010201 A	20020111	RECORDING AND REPRODUCTION
15	<input type="checkbox"/>	<input type="checkbox"/>	JP 01236787 A	19890921	RECORDING AND REPRODUCTION OF LUMINANCE SIGNAL
16	<input type="checkbox"/>	<input type="checkbox"/>	US 5986695 A	19991116	Recording method and apparatus for recording medium of security system

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 01-157180

(43)Date of publication of application : 20.06.1989

(51)Int.Cl.

H04N 7/01

(21)Application number : 62-315742

(71)Applicant : IIZERU:KK

(22)Date of filing : 14.12.1987

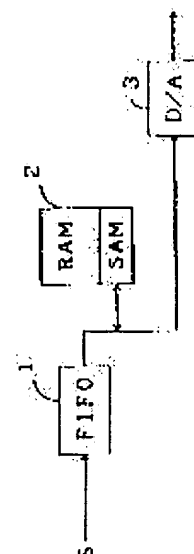
(72)Inventor : KUMAGAI RYOHEI

(54) SCAN CONVERTER

(57)Abstract:

PURPOSE: To decrease memory capacity by writing a next interlace image to a frame memory part, to which a reading is complete, or moving a raster in a line memory part to the frame memory part to which the reading is executed.

CONSTITUTION: The frame memory part, which can hold the interlace image equivalent to one picture, and the line memory part, which can hold an image for one raster part, are allocated to the RAM part of a dual port memory 2. The control of the dual port memory 2 is executed by an address reading and writing control means. Then, each time one raster part is read from the frame memory part, the next interlace image is written per raster to a memory area, in which the reading is completed, or, after the next interlace image is written per raster to the line memory part, the image equivalent to one raster is read from the frame memory part and the raster in the line memory part is moved to a storing area in which the reading is executed.



LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the
examiner's decision of rejection or application
converted registration]